

Fig.2(a)

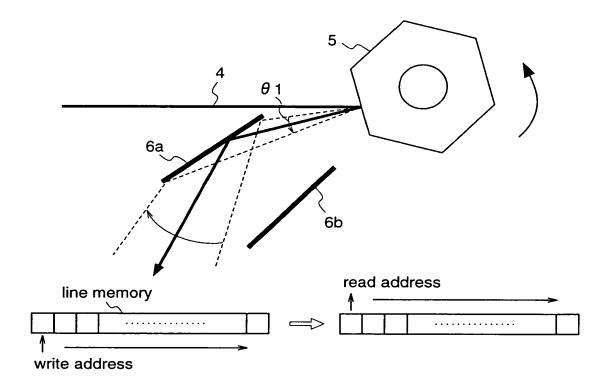


Fig.2(b)

fig.2(b)

fig.2(b)

read address

write address

Fig.2(c)

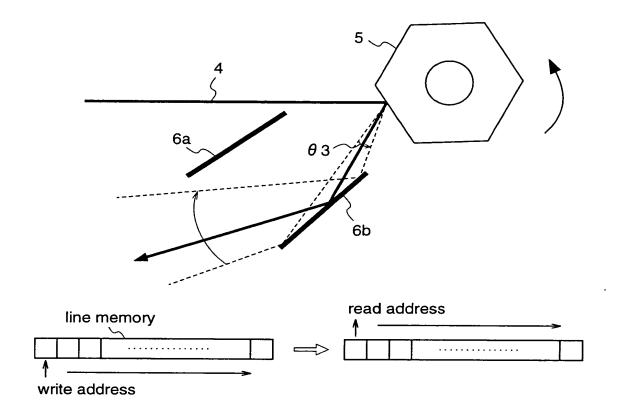


Fig.3(a)

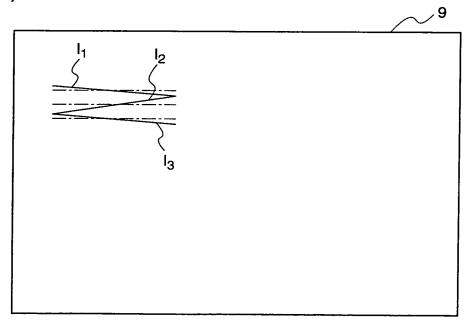


Fig.3(b)

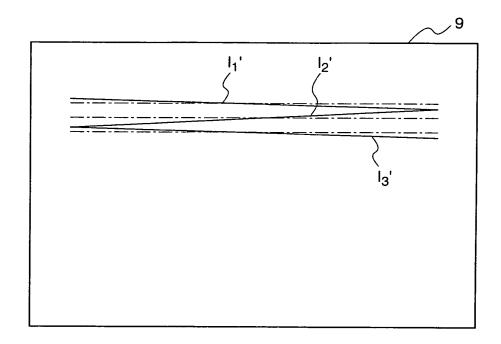
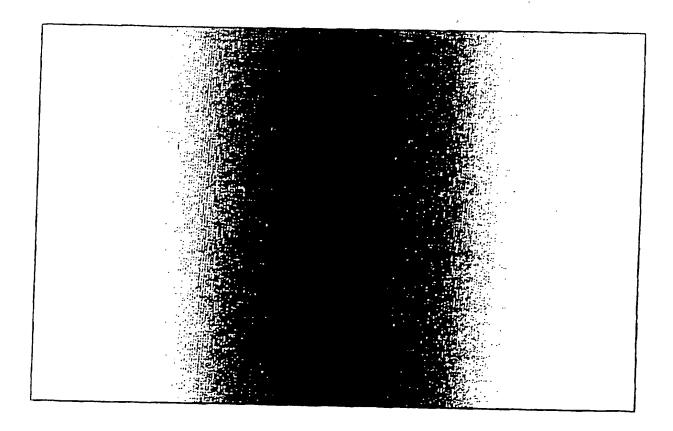


Fig.3(c)



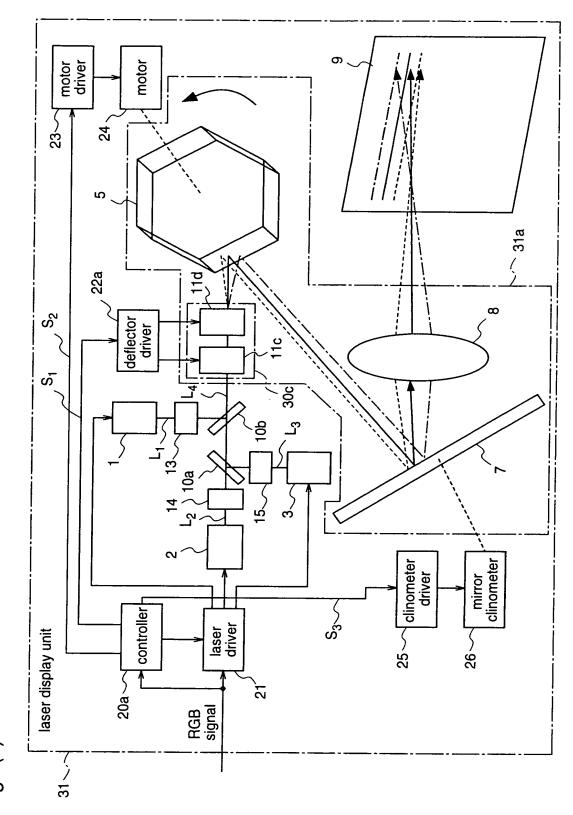


Fig.4(a)

Fig.4(b)

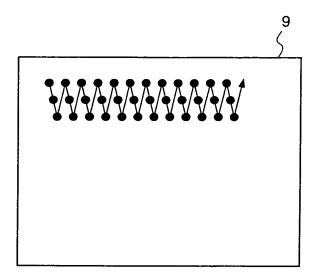
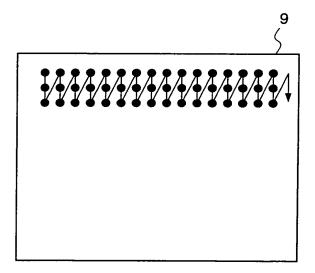


Fig.4(c)



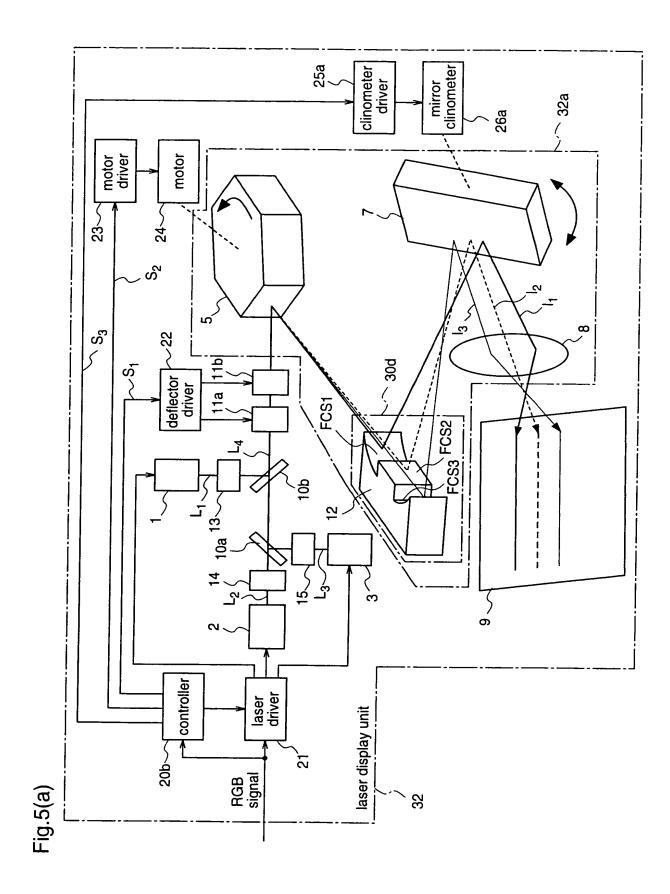
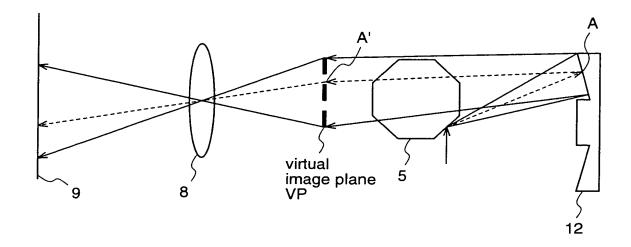
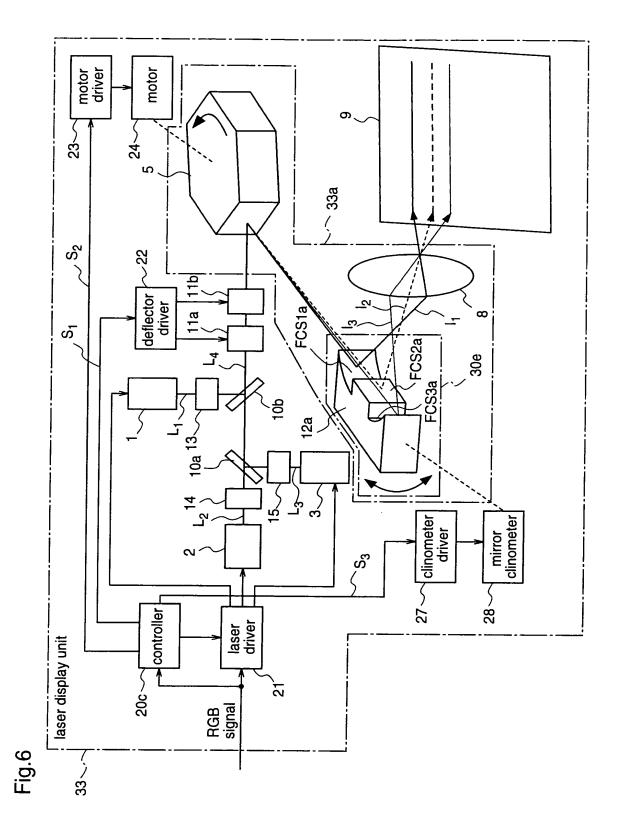


Fig.5(b)





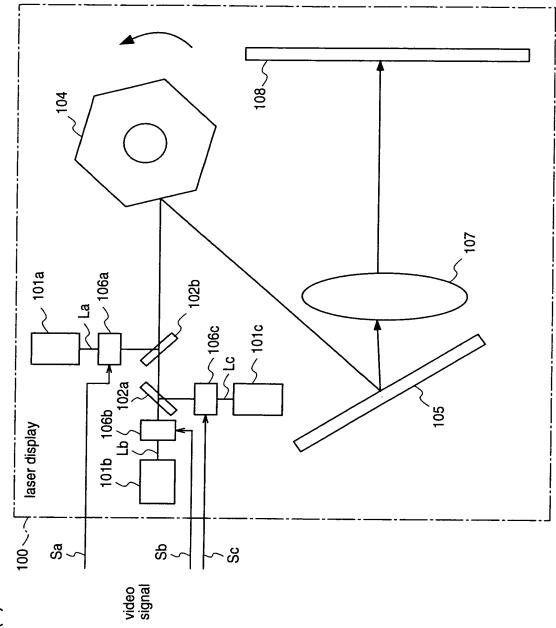


Fig.7(a)

